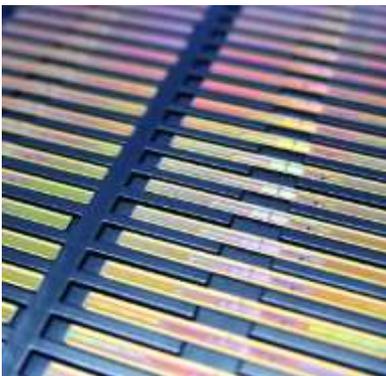


## PRESS RELEASE

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# EnSilica and Solomon Systech in multi-year eSi-RISC licensing deal

**Multi-year licensing agreement for EnSilica's highly-configurable, high-performance, low-power eSi-RISC processor IP covers multiple projects and architectures**



Photocaption: Utilizing EnSilica's eSi-RISC processor, Solomon Systech's SSD2098 Touch and Display Driver Integration (TDDI) IC targets FHD In-Cell LTPS panels for smartphones

**Wokingham, UK – 10<sup>th</sup> August 2017.** EnSilica, a leading independent provider of semiconductor solutions and IP, and Solomon Systech, a leading provider of touch and display ICs and system solutions, have entered into a multi-year licensing agreement for EnSilica's range of highly-configurable, high-performance, and low-power eSi-RISC processor IP. The multi-year licensing agreement covers the full range of EnSilica's 16 bit and 32 bit eSi-RISC processor IP and extends the existing, successful relationship between the two companies to cover multiple projects and multiple architecture implementations.

EnSilica's eSi-RISC processor IP has been adopted by Solomon Systech to power several of its smart touch screen controllers and drivers. Headquartered in Hong Kong with additional technology centers throughout Asia (China, Japan, Korea and Taiwan) and in Europe (United Kingdom), Solomon Systech operates on a fabless business model. The company specializes in the design, development and sales of proprietary IC products that enable a wide range of display applications for smartphones, tablets, smart TVs/monitors, notebooks and other smart devices, including wearables, healthcare devices, connected home devices, as well as industrial appliances, etc.

"With eSi-RISC already proven in several of Solomon Systech's existing products, we are extremely pleased to be extending our relationship by entering into a multi-year licensing agreement that covers multiple projects and multiple architecture implementations," said David Doyle, Commercial Director for EnSilica. "The performance, features and configurability of eSi-RISC are an ideal match for the enhanced functionality that Solomon Systech is seeking to deliver in its next generation products."

EnSilica's eSi-RISC is a highly configurable microprocessor architecture for embedded systems that scales across a wide range of applications. With either a 16 or 32-bit, 5-stage pipelined RISC, load-store architecture which is highly configurable and implemented in as little as 8k ASIC gates, the RISC IP core has been silicon proven in a variety of ASIC and FPGA technologies. Available in a choice of von Neumann or Harvard memory versions and using industry standard bus architecture (AMBA AXI/AHB/APB) for IP interconnection, intermixed 16 and 32-bit instructions give exceptional code density. Configurability and custom instructions deliver a solution with exceptionally low-power. eSi-RISC is implemented as a soft IP core, based on synthesisable Verilog RTL and can be easily ported to a wide range of ASIC processes and FPGAs. The design is also DFT ready, supporting full scan insertion for all flip flops and memory BIST.

Commenting further on the multi-year eSi-RISC licensing deal with Solomon Systech, Ian Lankshear, CEO of EnSilica added: "We are looking forward to eSi-RISC-based controller and driver ICs from Solomon Systech powering a new generation of display applications for smartphones, smart TVs and other smart devices."

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### **About EnSilica**

EnSilica was founded in 2001 and has a strong track record of success in delivering low power ASIC-based solutions with a focus on RF/Radio, Smart Sensing and/or Encryption/Security integration technology to OEM's and semiconductor companies worldwide. The company's headquarters are in the UK where it has three design offices as well as subsidiaries in Bangalore, India and Santa Clara, USA. In addition to providing turnkey ASIC development and supply chain services, EnSilica also provides point services to companies with in-house ASIC design teams covering system engineering, analog and RF design, and advanced verification using UVM, DFT and physical implementation. EnSilica also offers a highly successful portfolio of eSi-IP which comprises its eSi-RISC highly configurable and low power 16/32 bit embedded processors, eSi-Comms range of communications IP, eSi-ADAS automotive radar co-process IP, eSi-Connect range of processor peripherals and eSi-Crypto encryption IP. For further information about EnSilica, visit <http://www.ensilica.com>.

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### **Media contacts:**

David Doyle, Commercial Director for EnSilica  
Tel: +44 (0)1183 217 310. Email: [david.doyle@ensilica.com](mailto:david.doyle@ensilica.com)

Keith Mason, Humbug PR  
Tel: +44 (0)1305 849403. Email: [keith.mason@humbugpr.com](mailto:keith.mason@humbugpr.com)

Ref: ENS038  
Words: 429

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