

Stencil Design for Lead-Free SMT Assembly

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Abstract

In order to comply with RoHS and WEEE directives, many circuit assemblers are transitioning some or all of their soldering processes from tin-lead to lead-free within the upcoming year. There are no drop-in replacement alloys for tin-lead solder, which is driving a fundamental technology change. This change is forcing manufacturers to take a closer look at everything associated with the assembly process: board and component materials, logistics and materials management, solder alloys and processing chemistries, and even soldering methods.

Do not expect a dramatic change in soldering behavior when moving to lead-free solders. The melting points of the alloys are higher, but at molten temperatures the different alloys show similar behaviors in a number of respects. Expect subtler changes, especially near the edges of a process window that is assumed based on tin-lead experience rather than defined through lead-free experimentation. These small changes, many of them yet to be identified and understood, will manifest themselves with lower assembly yields. The key to keeping yields up during the transition to lead-free is quickly learning what and where the subtle distinctions are, and tuning the process to accommodate them.

Introduction

The lower spread of lead-free solder is a well documented behavioral difference that will require some adjustment. In the particular study described below, the investigators were concerned by the lower spread of lead-free alloys, especially on organic solderability preservative (OSP) surface finishes. In the world of tin-lead soldering, it is a common practice to design stencil apertures that are

slightly smaller than their mating pads. If the entire pad is not covered with paste, that's okay – it will most likely spread out over the pad in the reflow process. In the world of lead-free soldering the alloy most likely will *not* spread out to cover the pad, leaving some of the copper pad exposed in the final assembly. Figure 1 illustrates the differences between the alloys on 0603 and 0805 pads.

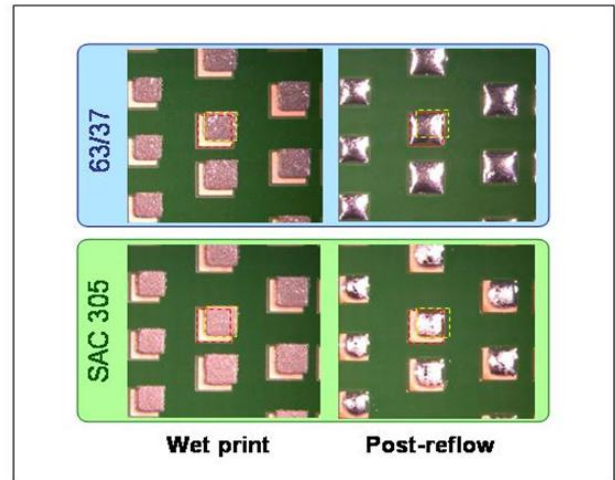


Figure 1. Photographs of 0603 and 0805 solder paste prints intentionally offset to illustrate the lower spread properties of lead-free solder alloys. Note that the SAC305 stays right where it was printed.

Granted, exposed copper is purely a cosmetic concern and has not been shown to adversely affect reliability, but it is still a concern for many assemblers and their customers. The practice of cropping stencil apertures is used not only to improve fine-pitch print yields, but also to limit the amount of solder deposited onto the pads of passive components. Limiting the amount of paste helps minimize mid-chip solder ball and tombstone defects. If a current aperture design cannot be successfully downsized to limit quality problems without creating new cosmetic concerns, new

aperture designs must be developed that address both areas.

The investigators of the following study set out to benchmark current stencil design performance and identify optimized aperture designs for lead-free solder pastes. Baseline data was generated using the known best and worst performing tin-lead stencil designs. They then introduced three new aperture designs, identified the best performer, and did some further investigation to verify its merit.

The Investigation

The aperture design study was undertaken in two portions. The first portion benchmarked the performance of aperture designs known to be the best and worst cases for the formation of mid-chip solder balls (MCSBs) in tin-lead soldering. These designs were built with both tin-lead and lead-free solder pastes. The second portion sought to improve upon the original apertures’ performance, using lead-free paste only.

The test vehicle in this study had 100 IPC-recommended pad stacks for 1206, 0805, 0603, and 0402 devices, for a total of 400 devices per assembly. 50 of each device were mounted horizontally; 50 were mounted vertically. The experimental design in both portions was a full factorial with three replicates. There were a total of 1200 data points per condition set.

The factors in Part I included:

- Solder Type – tin-lead or SAC305
- Profile Type – ramp or soak shown in Figure 2.
- Surface finish – OSP, ENIG, Immersion Silver (ImAg) or Immersion Tin (ImSn)
- Stencil aperture designs (Figure 3) – benchmarked best and worst cases.

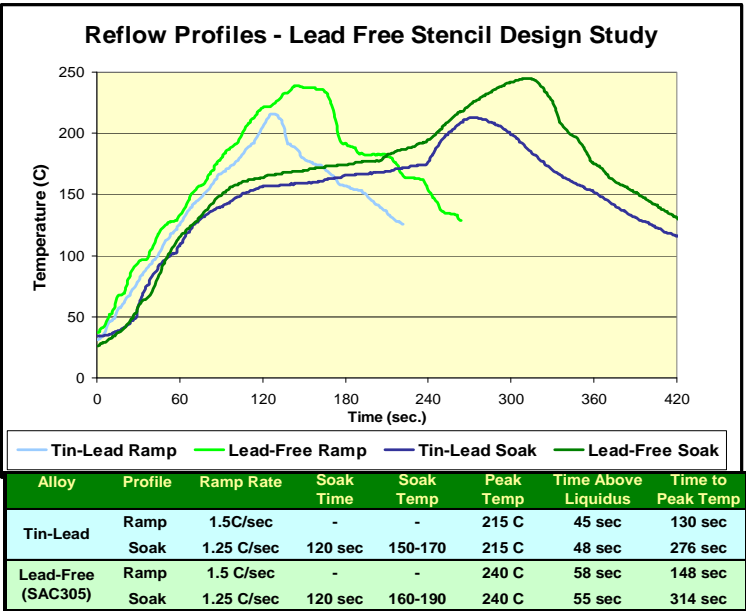


Figure 2. Profiles used in Parts I and II of the study

Typical Chip Component Stencil Apertures

Square/Rectangular
Same shape as pad
Printed at 1:1 with pad
or at 10% reduction

Home Plate
Inside corners cut back
to limit paste volume
Printed at 1:1 with pad
or at 10% reduction

Inverted Home Plate
Not used or recommended
Paste can stick in tight corners
Pointed area in center can cause
stencil or squeegee damage

Figure 3. Typical Stencil Apertures. Rectangular and Home Plate were used in Part I of the study.

The best case, was a home plate design with the aperture area reduced by 10% on a 5 mil foil. The worst case was a rectangular apertures at 1:1 with the pad on a 6 mil foil.

While the overall average number of MCSB's in Part I averaged 32.6 per board, equating to a device yield of only 91%, the vast majority of MCSB's were produced with the worst-case stencil design.

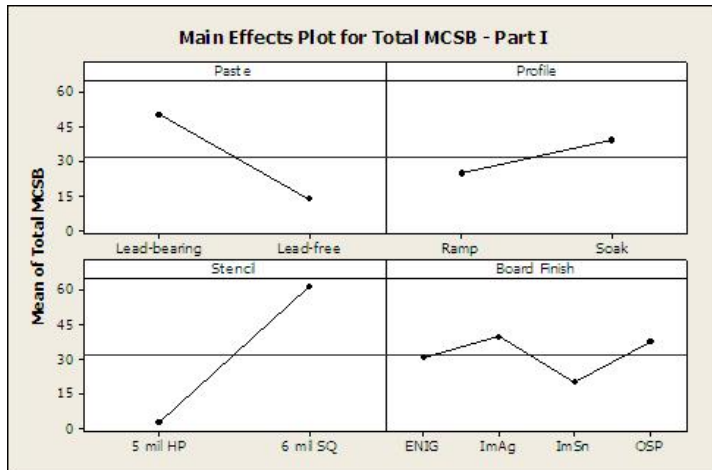


Figure 4. Main Effects for Part I. Notice the decrease in mid-chip solder balls with lead-free solder paste.

The best-case stencil design averaged only 2.67 MCSB's per board, or a device yield of 99%.

The main effects plot (figure 4) shows other interesting effects:

- Lead-free solder paste shows far less propensity to form MCSBs than tin-lead.
- As expected, the ramp profile produced less solder MCSBs than the soak profile.
- Although the magnitude of the effect surface finish was not as great on the formation of MCSBs as other factors like alloy type and stencil design, the effect was still significant. Immersion tin finishes produced the least amount of MCSBs, while OSP and immersion silver produced more. The ENIG finish produced an average amount of MCSBs.

The factors used in Part II of the study included:

- Profile Type – ramp or soak
- Surface finish – OSP or Immersion Tin (ImSn)
- Stencil thickness – 5 or 6 mil foil
- Stencil aperture shape – three experimental designs developed through a previous series of screening experiments. The three apertures are shown in Figure 5 (right):

- 1) Radiused home plate
- 2) Radiused inverted home plate with proportions 20%-60%-20%
- 3) Radiused inverted home plate with proportions 30%-40%-30%

All experimental apertures were at 1:1 proportions with the pads in order to limit exposed copper at the edges.

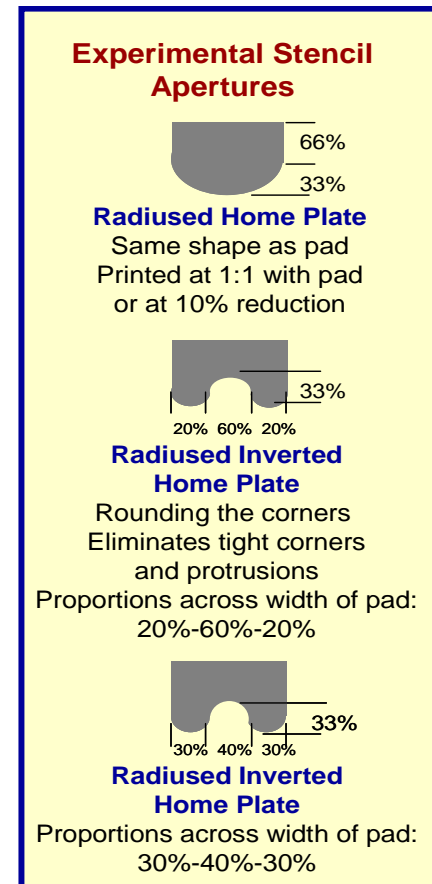


Figure 5. Experimental aperture designs

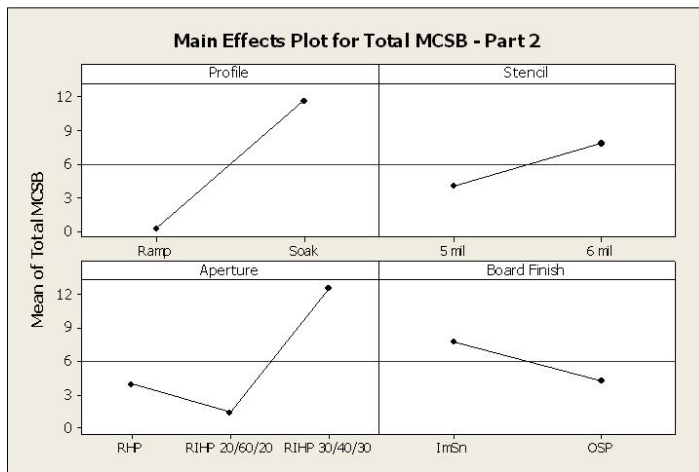


Figure 6. Main effects plot for Part II. Notice the dramatic effects of paste volume and profile

The average number of mid-chip solderballs produced per board was 6.5, or a device yield of 98%. Observations from figure 6 include:

- Aperture shape has far more influence on MCSB formation than foil thickness.
- The effect of profile type is approximately equivalent to that of aperture shape.
- With SAC305 pastes, the immersion tin appeared to produce slightly more MCSB's than OSP surface finishes, but the effect is not very severe.

The best aperture identified was the radiused, inverted home plate (RIHP) with aperture proportions 20%-60%-20%. It produced an average of 1.45 MCSBs per board, or a 99.5% device yield. This represents a 45% reduction in mcsb's when compared to the previously benchmarked best case of 2.67 MCSBs per board. Note that this average *includes* the influence of 6 mil foils in its calculation!

Just because an aperture shape remedies one problem, it should not be assumed that it is automatically fit for use. Areas which merited further investigation before the aperture could be recommended include tombstoning, volume and repeatability of the deposits, and spread underneath the component termination.

Tombstoning performance

Tombstoning has many causes, most of which were not included in this study. But aperture design is a factor in tombstoning, and the investigators wanted to insure that a reduction in MCSBs was not achieved at the cost of increased tombstones.

In Part I, all the tombstone defects occurred with lead-free solder paste. The effect of the alloy was the clearly the main driver of the results. Tombstoning rates averaged 1.2 defects per board (recall there are 400 devices per board) for SAC305 paste and 0 defects per board for tin-lead paste. The defect trend of the lead-free solder alloy followed behavior expected in tin-lead systems: it increased substantially with higher solder volumes, particularly when combined with fast ramp profiles. But the SAC305 was far more sensitive to higher solder volumes than the tin-lead. This indicates that the lead-free window is much tighter than the tin-lead window with respect to thicker foils and excessive paste deposits as they relate to tombstone defects.

In Part II of the study, the RIHP 20-60-60 aperture shape produced no tombstones. Again, Part II used only lead-free solder paste, so the defect rate was reduced from 1.2 defects per board to 0 defects per board. Insufficient data exists to state that this is an optimum aperture design to minimize tombstoning, but the results indicate a discernable improvement over the two baseline designs. The investigators are planning further studies on tombstoning to characterize its causes in lead-free systems and with smaller devices.

Sufficient, repeatable solder volumes

Interestingly, the RIHP 20-60-20 (known hereafter as simply the "RIHP") design deposits more solder paste than the homeplate designs used in the study. Depending on the device, a RIHP aperture provides from 6 to 20% more paste volume when printed with a 5 mil foil and from 20 to 40% more volume when printed with a 6 mil foil.

Although 0402 devices are not particularly difficult to print, they were chosen for volumetric analysis because they were the smallest feature printed in the study. The roughly 3000 cu mil deposits have about the same volume as a single 20 mil pitch (QFP208) deposit.

Stencil Aperture	Avg. Volume (cu mils)	Std. Dev. % of mean
5 mil HP 10%	3093	3%
5 mil RIHP 20-60-20	3275	4%
6 mil RIHP 20-60-20	3734	4%
6 mil SQ	5439	3%

Table 1. Solder Paste Volumes for 0402 devices

Table 1 shows the average volumes of 400 features, and the standard deviations of the volumes expressed as a percent of the mean. The 5 mil RIHP aperture provided 6 percent more paste volume and the 6 mil RIHP provided 20% more paste volume - yet neither produced more mid-chip solder balls - than the benchmark tin-lead aperture design.

The RIHP showed a slightly higher variability in volume consistency than the regular homeplate design. A commonly used rule-of-thumb on print repeatability states that standard deviation should be no more than +/- 10% of the mean volumes. At 3-4%, all four aperture designs showed good volumetric repeatability.

Solder spread under the component

X-ray analysis was performed to assure that the lead-free solder did spread underneath the components' bodies to the ends of their terminations. Figure 7 shows an x-ray image of the most challenging spread conditions: a 1206 component (farthest distance to spread), printed with a 5 mil foil (least solder volume) and reflowed under a ramp profile (less opportunity for hot slump).

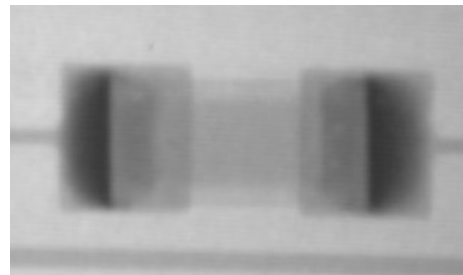


Figure 7 – X-ray image of 1206 device

Conclusion

Converting to lead-free circuit assembly will require some adjustments. The principles and behaviors of solder that are well understood still hold true, but subtle differences in the behavior of the new alloys will call for some fine tuning. The tuning methods will be the same ones used in tin-lead soldering for decades: the application of statistical methods, scientific knowledge and common sense.

This lead-free aperture design study assembled over 67,000 components and produced nearly 3600 solder balls. It was determined that lead-free pastes are more forgiving than tin-lead on mid-chip solder balls, but less forgiving on tombstones, where they showed a higher sensitivity to excessive solder volumes.

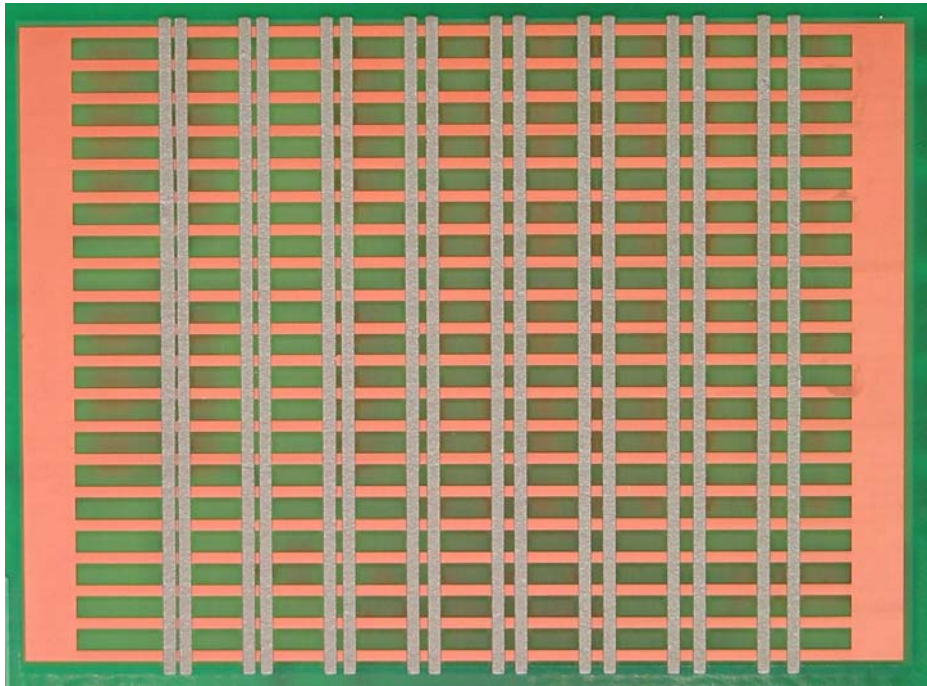
The stencil aperture that performed best was the radiused inverted home plate (RIHP), which reduced the number of mid-chip balls by nearly half when compared to the home plate design. The RIHP was used successfully in both 5 and 6 mil foils, and employs a 1:1 aperture to pad ratio. Further investigation showed that it did not increase incidence of tombstones, but it did offer more solder volume than the home plate with similar repeatability, and allowed for sufficient spread underneath component terminations.

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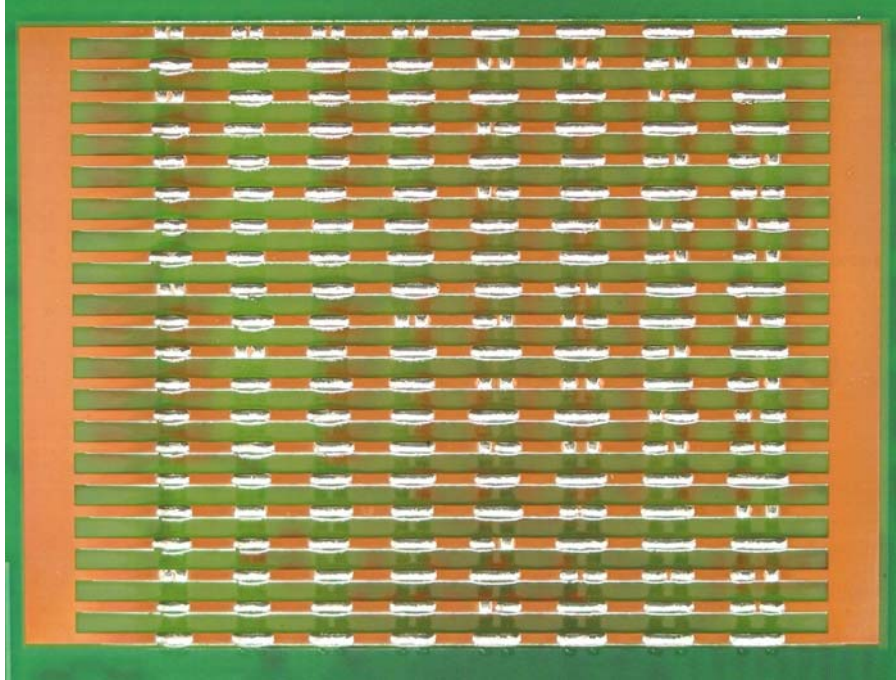
Appendix A – Quantifying Lead-Free Solder Spread

The commonly employed method of printing a circle of known diameter and measuring its spread after reflow worked very well in the world of tin-lead. But with lead-free alloys, the lower rate of spread, combined with an uneven pattern of the spread, renders this test rather helpless in quantifying or comparing the spread of the alloys on different surface finishes.

An alternative solder spread/wetting test involves printing pairs of uniform thickness (40 mil) solder paste stripes onto an array of uniform thickness (30 mil), equally spaced traces on the board. The paste stripes have increasing gap sizes between them and are printed in a direction perpendicular to that of the traces, as shown below.



During reflow the molten solder expands along the metal lines on the board. If there is enough spread, the gap between two adjacent solder spots is bridged. The gaps between solder paste stripes are varied from 0.1 mm to 0.8 mm.



There are a maximum number of twenty bridges possible for each gap size. The number of gaps to bridge is counted and the results are plotted as a function of the gap size. The results are shown below:

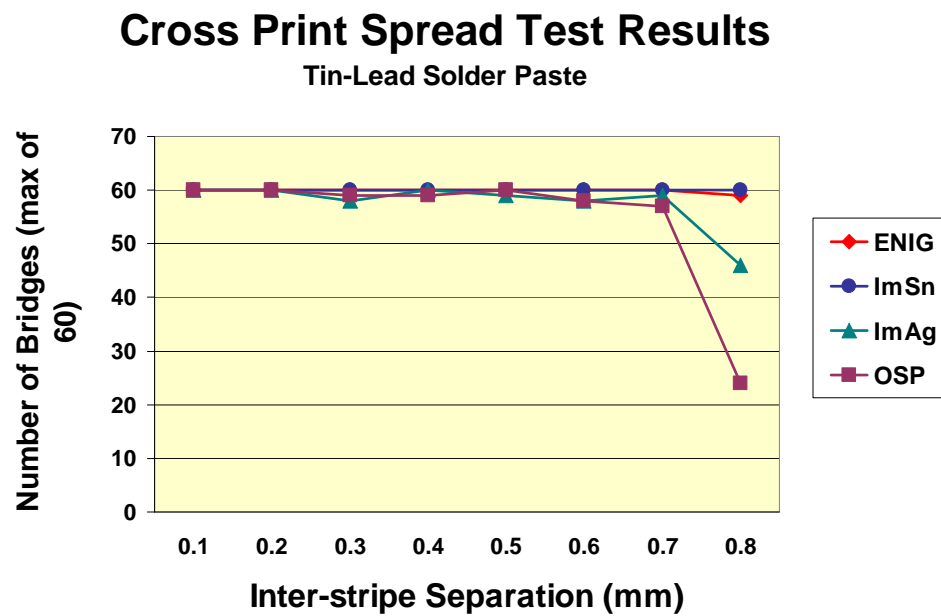


Figure 1. Cross print test showing the number of bridges formed by lead-bearing paste printed on four different board finishes.

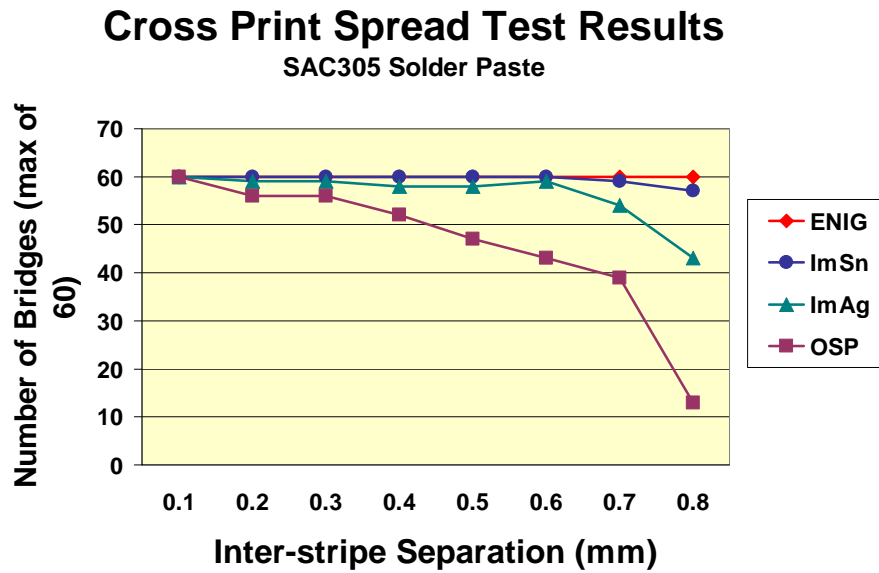


Figure 2. Cross print test showing the number of bridges formed by lead-free paste printed on four different board finishes.

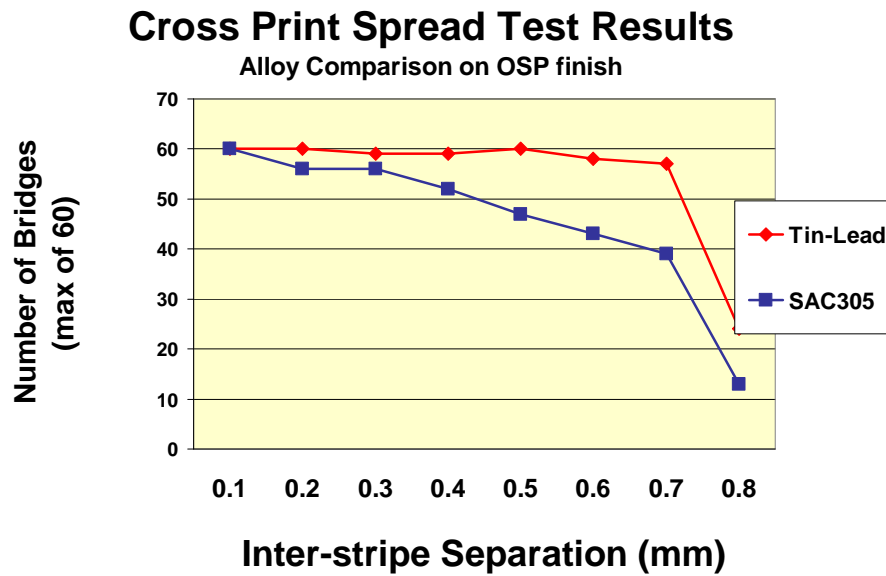


Figure 3. Difference in spread of lead-free and lead-bearing pastes in OSP surface finish as observed in the cross print pattern test.